

FIG. 1A

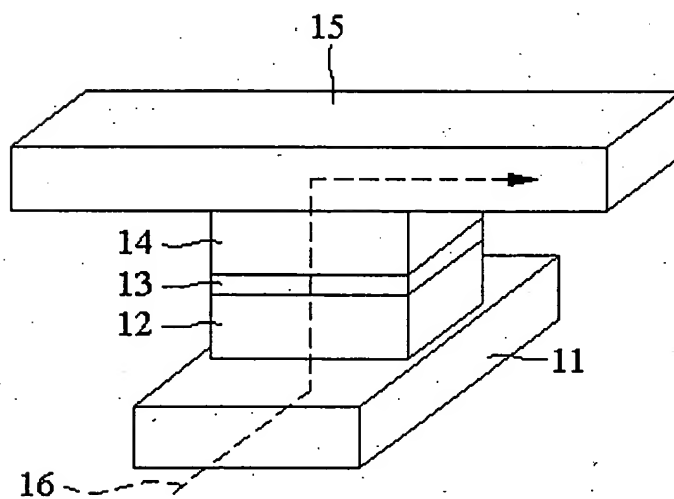


FIG. 1B

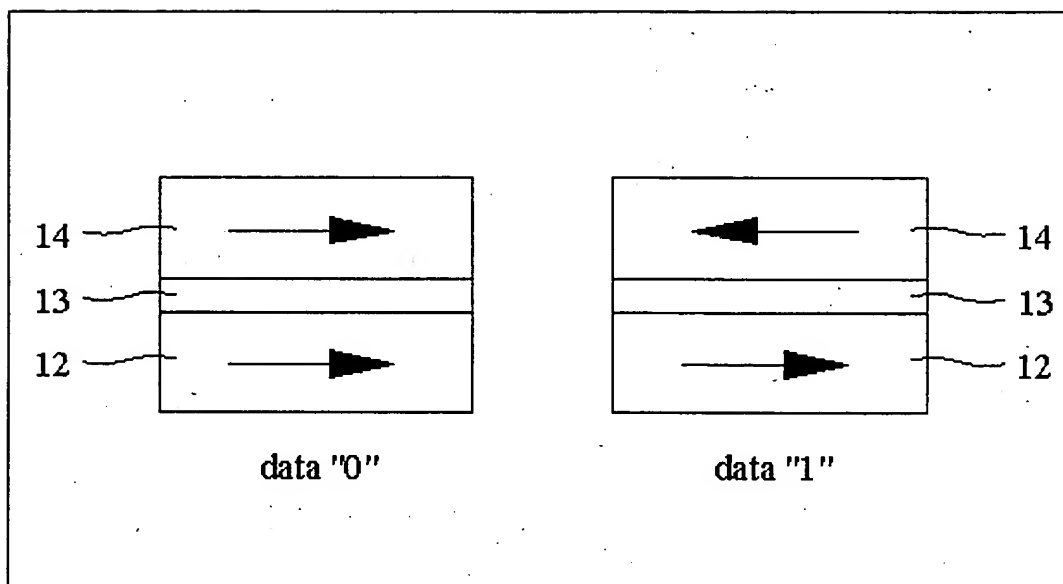


FIG. 1C

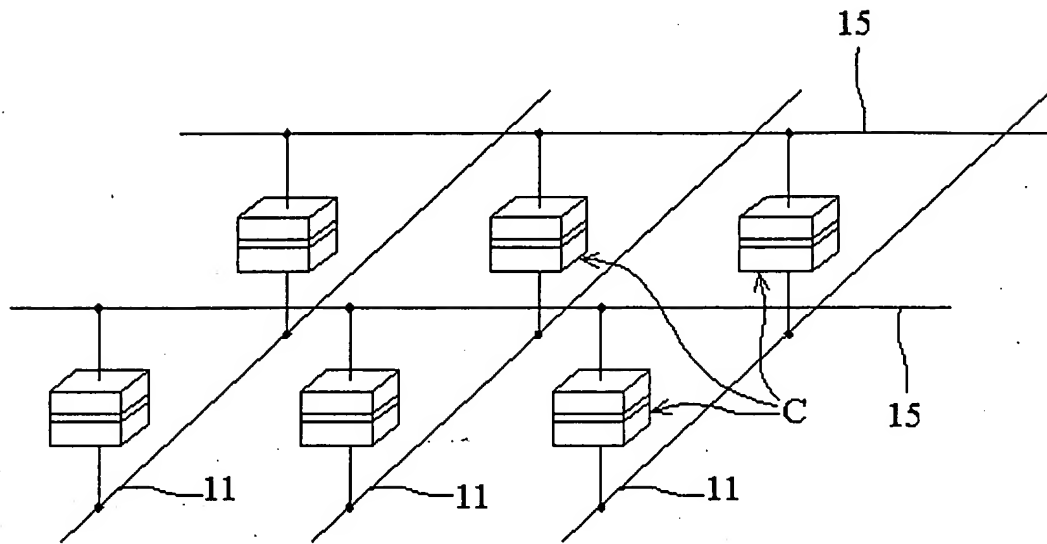


FIG. 2A

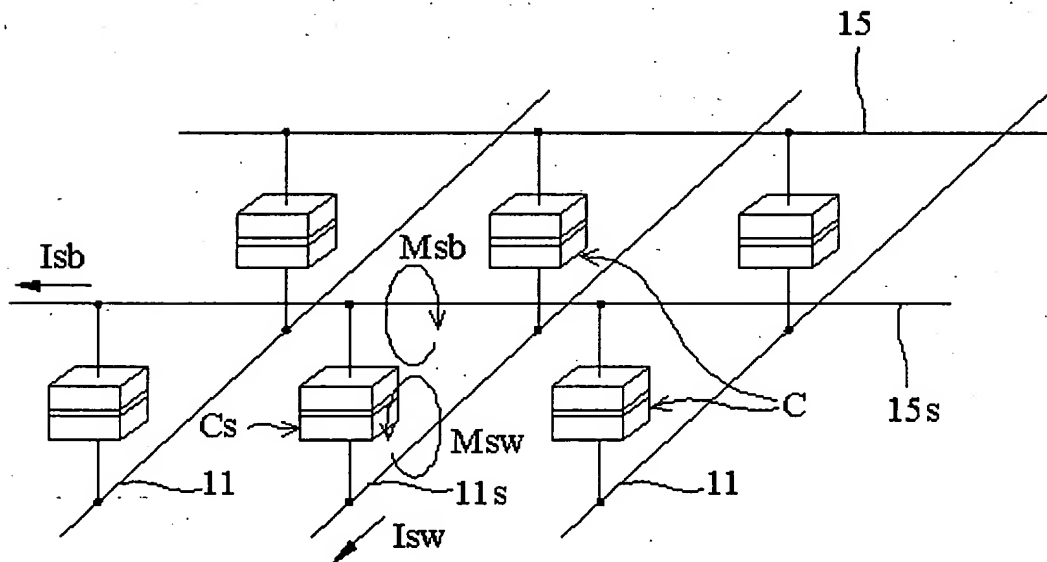


FIG. 2B

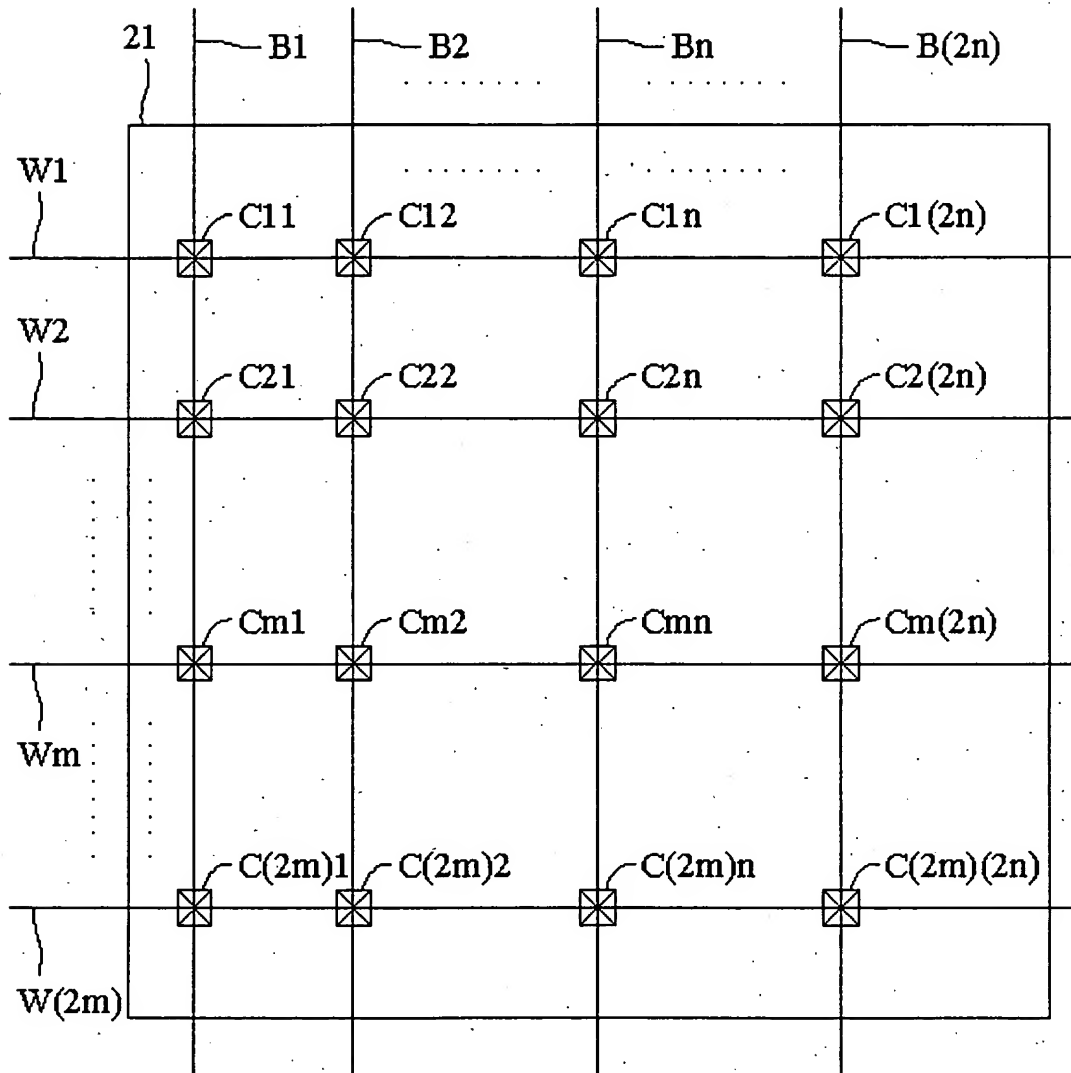


FIG. 3

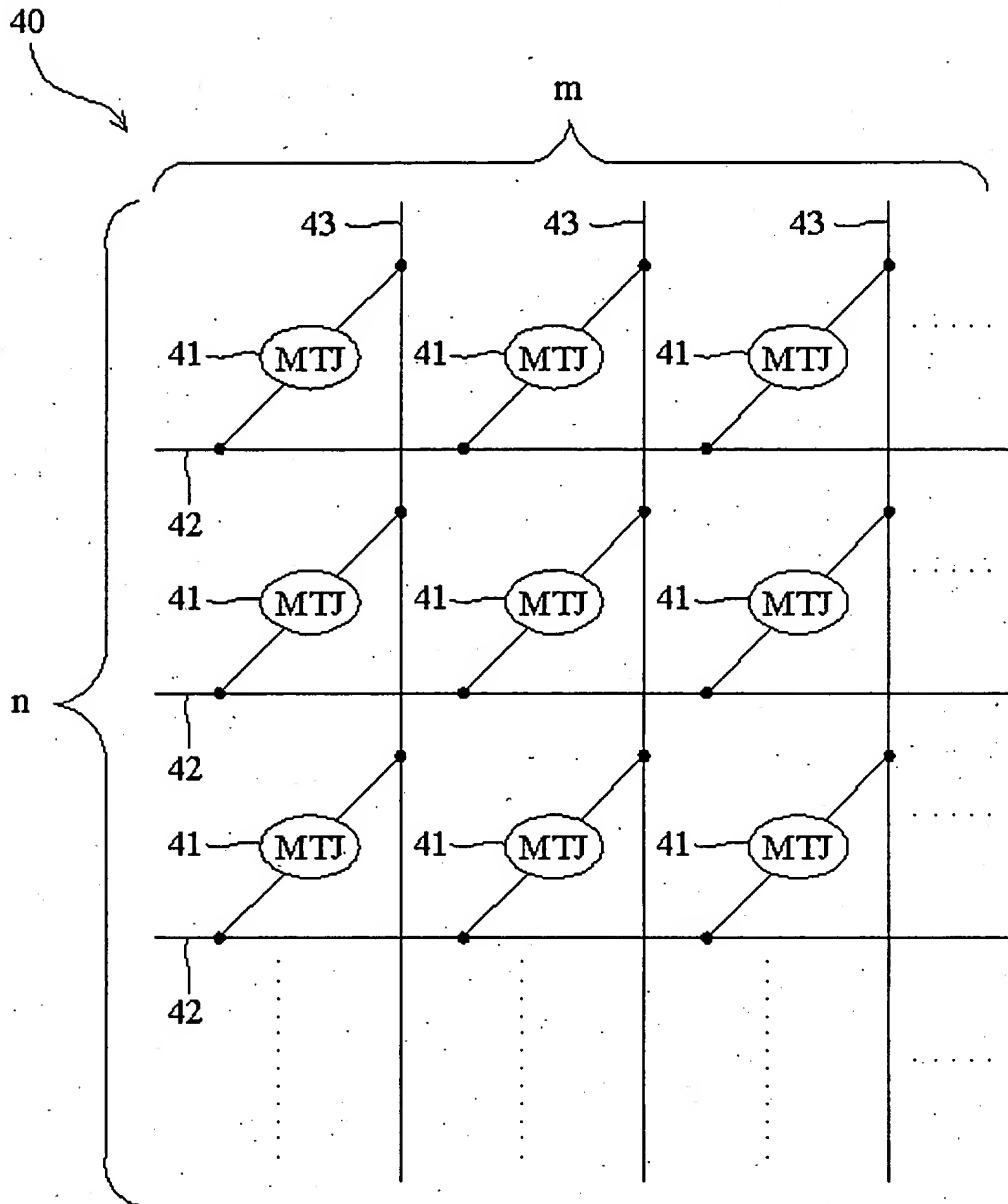


FIG. 4

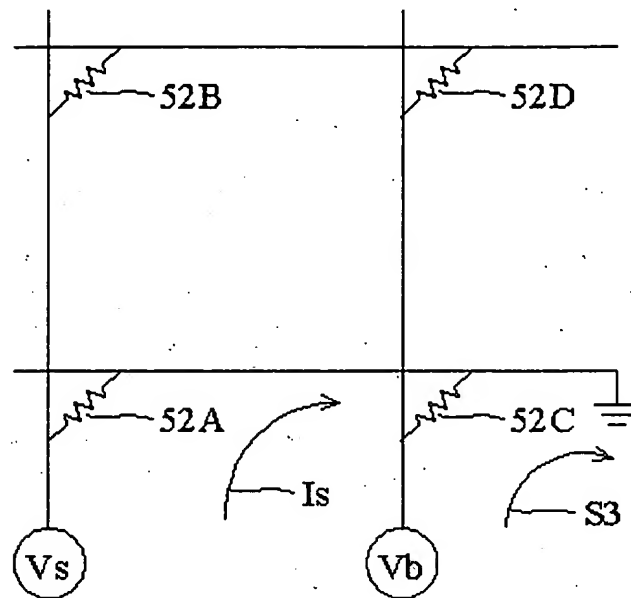


FIG. 5A

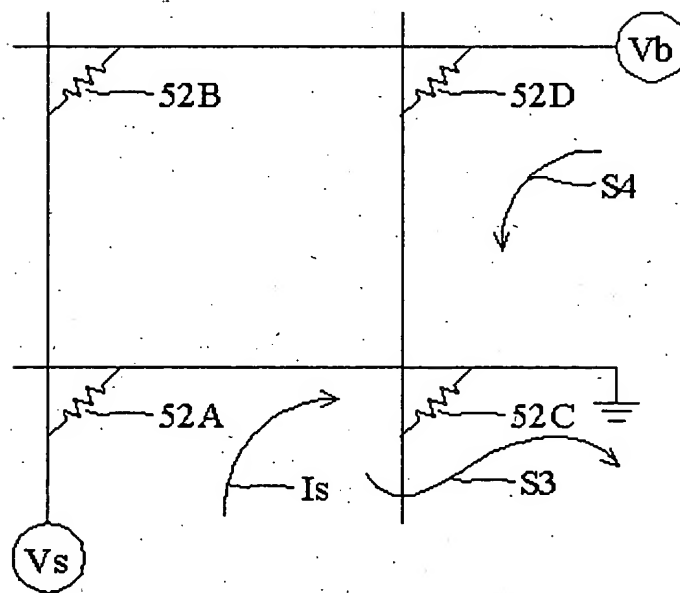


FIG. 5B

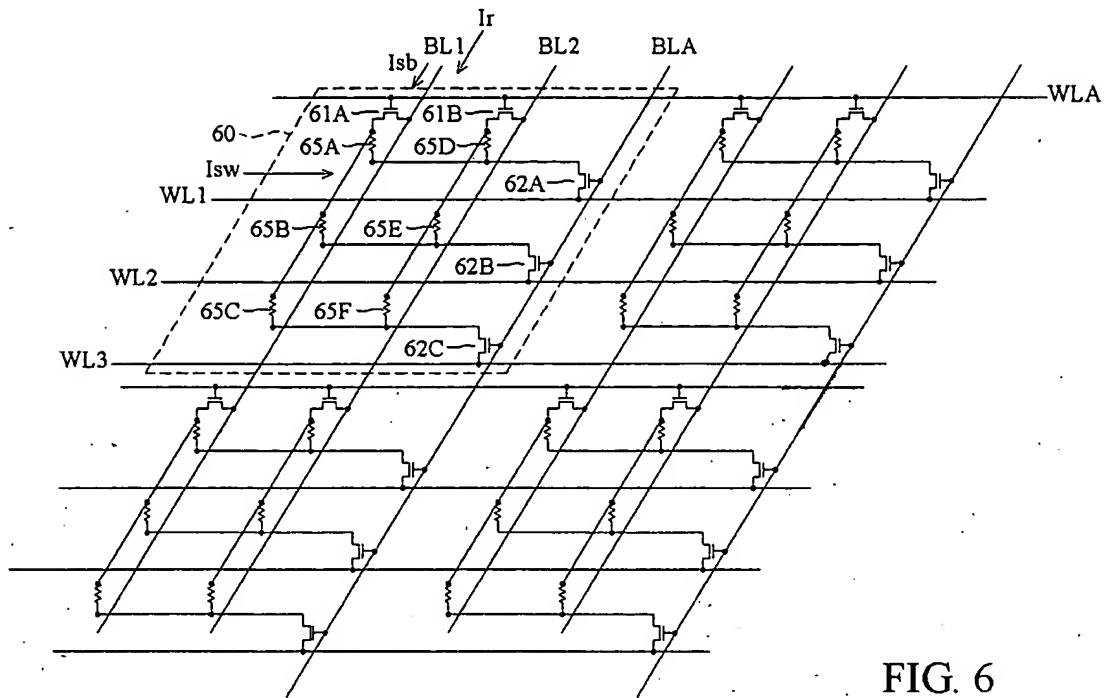


FIG. 6

SEGMENTED MRAM MEMORY ARRAY
Attorney Docket No. 24061.163 (TSMC2003-0276)
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3X2 array	Row=3, Column=2					
Target	The others	Resistor 52B	Resistor 52C	Resistor 52D	Sneak Impedance	Total R
1X	1X	0.5X	1X	0.5X	2.0X	0.6667X
1X	1.4X	0.7X	1.4X	0.7X	2.8X	0.7368X
1.4X	1X	0.5X	1X	0.5X	2.0X	0.8235X
1.4X	1.4X	0.7X	1.4X	0.7X	2.8X	0.9333X

FIG. 7

3X2 array	Row=3, Column=3					
Target	The others	Resistor 52B	Resistor 52C	Resistor 52D	Sneak Impedance	Total R
1 X	1 X	0.5 X	0.5 X	0.25 X	1.250 X	0.5556 X
1 X	1.4 X	0.7 X	0.7 X	0.35 X	1.750 X	0.6364 X
1.4 X	1 X	0.5 X	0.5 X	0.25 X	1.250 X	0.6604 X
1.4 X	1.4 X	0.7 X	0.7 X	0.35 X	1.750 X	0.7778 X

FIG. 8

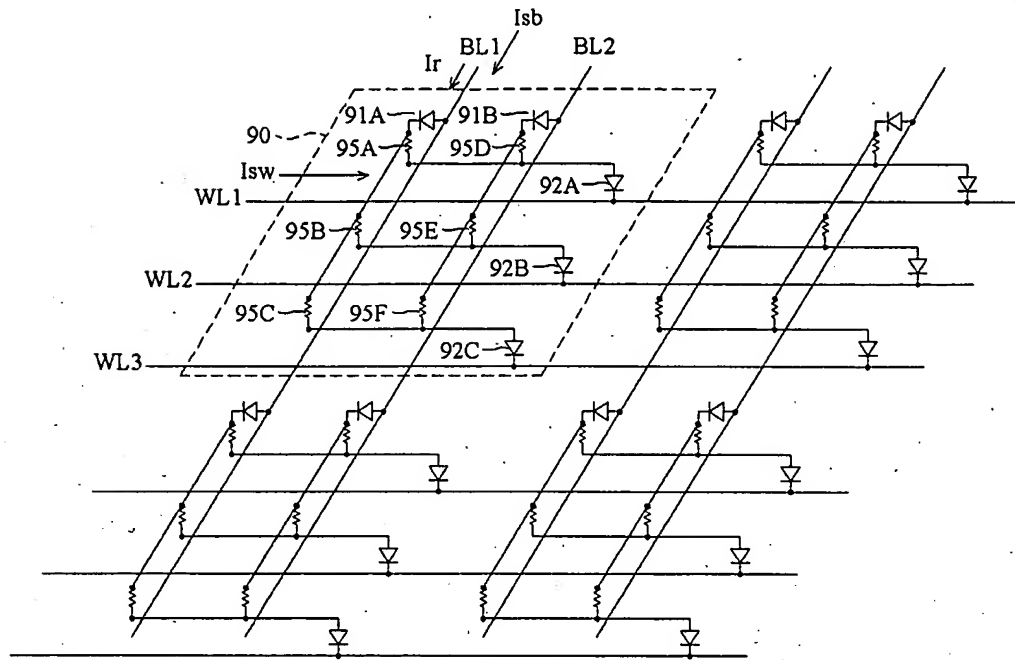


FIG. 9